

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| Applicant | : Gilbert Wolrich et al. | Art Unit | : 2154 |
| Serial No. | : 10/069,229 | Examiner | : Joshua Joo |
| Filed | : December 11, 2002 | Confirmation No. | : 1429 |

Title : BRANCH INSTRUCTION FOR PROCESSOR ARCHITECTURE

MAIL STOP AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Entered
JJ

REPLY TO FINAL ACTION OF MARCH 17, 2006

3/15/06

Please amend the above-identified application as follows: